



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 6066

SERIAL NUMBER 10/619,136	FILING DATE 07/14/2003 RULE	CLASS 324	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. X-1269-1P US
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

David Mark, San Jose, CA;

Yung-Cheng Chen, San Jose, CA;

Randy J. Simmons, San Jose, CA;

** CONTINUING DATA *****

This application is a CIP of 10/355,563 01/31/2003 PAT 6,788,095

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/08/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 6	TOTAL CLAIMS 26	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>[Signature]</i>	INITIALS <i>[Initials]</i>		
Verified and Acknowledged				

ADDRESS

24309
 XILINX, INC
 ATTN: LEGAL DEPARTMENT
 2100 LOGIC DR
 SAN JOSE, CA
 95124

TITLE

Method for gross I/O functional test at wafer sort

FILING FEE RECEIVED	FEEs: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
----------------------------	---	--